

Electronic Acknowledgement Receipt

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Title of Invention:	SEMICONDUCTOR INTEGRATED CIRCUIT
First Named Inventor/Applicant Name:	Yasushi Kinoshita
Customer Number:	23373
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)
1		q77597.pdf	1104831	yes	19
			37a441f51ccb0c7e969aa1fd67e9eb3e365f767c		

	Multipart Description/PDF files in .zip description		
	Document Description	Start	End
	Information Disclosure Statement Letter	1	2
	NPL Documents	3	7
	Foreign Reference	8	13
	Foreign Reference	14	19

Warnings:

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If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

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If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

New International Application Filed with the USPTO as a Receiving Office

If a new international application is being filed and the international application includes the necessary components for an international filing date (see PCT Article 11 and MPEP 1810), a Notification of the International Application Number and of the International Filing Date (Form PCT/RO/105) will be issued in due course, subject to prescriptions concerning national security, and the date shown on this Acknowledgement Receipt will establish the international filing date of the application.